




FACULTY OF ENGINEERING & ELECTRONIC
FKEE
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Technically
Above The Rest 

Student Assessment System (SAS) – OBESys – TCIS

TRANSFORMASI
MINDA 

FACULTY OF ENGINEERING & ELECTRONIC
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SAS – OBESys

- The following slides describe about things that must be done by the course coordinator regarding Student Assessment System (SAS)
- Please ensure the details in the SAS regarding the course taught are correct so that the reports generated by the OBESys are right and accurate
- This is very important for OBE reporting and accreditation purposes

Login to TCIS
 Then go to:
MENU - STAF AKADEMIK
 Click:
Student Assessment System
 Then click:
Course Co-ordinator Module

Under Course Co-ordinator Module:
 Key in **Session** and **Programme**

Your **course(s)** for that particular session will appear

Sl	Session	Sem	Course Code	Course Name	Submitted / No. Of Section	No. Of Student	Course Coordinator	Total (%) Assessment
1.	20152016	2	BED30303	REKABENTUK VLSI / VLSI DESIGN	0 / 1	54	K000623 - PROF. MADYA SITI HAWA BINTI RUSLAN	100

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Course Co-ordinator Module:
Under Tab : **OBE Matrix**, Please ensure the followings:

COURSE CO-ORDINATOR MODULE

Input
Session : 20152016 / 2 Programme : Degree User Manual : Manual Penyelaras

Course List

Bil	Sesi	Sem	Course Code	Course Name	Submitted / No. Of Section	No. Of Student	Course Coordinator	Total (%) Assessment
1	20152016	2	BED30303	REKABENTUK VLSI / VLSI DESIGN	0 / 1	54	K000623 - PROF. MADYA SITI HAWA BINTI RUSLAN	100

OBE Matrix Assessment Management Assessment Report By Section List Of Lecturers CQI Report Documents (In Progress)

OBE Matrix (CLO-PLO)

Kod Kursus : BED30303 Pilihan OBE : 2 Keterangan : Penetapan mulai 2015/2016

20122013 BED1011-1 KEMASUKAN SESI 2012/2013- KOD KURSUS 5 DIGIT (PERUBAHAN UWB10101 KEPADA UWB10102)-BED

CLO	PLO	Weight	Requirement	
CLO 1	PLO 04 - Critical Thinking and Problem Solvnr	50	55	Design correctly VLSI circuits using the right method and technique to meet a specified need/requiremen
CLO 2	PLO 10 - Design [DS]	50	55	Optimise efficiently the design parameters in designing VLSI systems.
CLO 3	PLO 02 - Practical Skills [P]	50	55	Use effectively modern CAE/CAD tools to design VLSI circuits.
CLO 4	PLO 09 - Leadership Skill [LS]	50	55	Demonstrate the ability to lead the current design and techniques in VLSI system.

Mapping of CLO to PLO must be exactly as stated in the syllabus

KPI : 50 % of students must achieve 55

The CLOs displayed must be exactly as stated in the syllabus

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Please inform FKEE's Teaching & Learning Committee (JK P&P) if the information displayed is incorrect. Call AP Siti Hawa at 7363 or e-mail sitihawa@uthm.edu.my

Syllabus to be followed is the one published in **January 2015** for BEJ programme and **August 2014** for BEV programme

Kod Kursus (Course Code): BED 30303	Nama Kursus (Name of Course) : VLSI DESIGN		
Kursus Pra Syarat (Prerequisite Courses) : BEC 30503 DIGITAL DESIGN	Kredit (Credit): 3	Kuliah (Lecture): 3	Amali (Practical): 0
Edisi (Edition) : 3	Tarikh Keluaran (Published date) : January 2015		
MATLAMAT (GOALS): To provide an in-depth understanding of VLSI design.			
HASIL PEMBELAJARAN (LEARNING OUTCOMES): At the end of this course the student will be able to:			
<ol style="list-style-type: none"> design correctly VLSI circuits using the right method and technique to meet a specified need/requirement; (PLO4 – CTPS – C4) optimise efficiently the design parameters in designing VLSI systems; (PLO10 – DS – C4) use effectively modern CAE/CAD tools to design VLSI circuits; and (PLO2 – PS – P4) demonstrate the ability to lead the current design and techniques in VLSI system. (PLO9 – LS – A3) 			

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Course Co-ordinator Module:

Under Tab : **Assessment Management**

List all assessments to be made (Name and Method) and the CLOs to be measured, along with the percentage.

Example of a course with 4 CLOs

NO.	ASSESSMENT NAME	METHOD	CLO	FULL MARK	PERCENTAGE
1.	DESIGN 1	ASSIGNMENT	CLO 3	4.00	4.00
2.	DESIGN 1	ASSIGNMENT	CLO 4	4.00	4.00
3.	DESIGN 2 - A	ASSIGNMENT	CLO 4	4.00	4.00
4.	DESIGN 2 - C	ASSIGNMENT	CLO 2	4.00	4.00
5.	DESIGN 2 - P	ASSIGNMENT	CLO 3	4.00	4.00
6.	TEST 1	TEST	CLO 1	35.00	8.75
7.	TEST 1	TEST	CLO 2	25.00	6.25
8.	TEST 2	TEST	CLO 1	40.00	10.00
9.	TEST 2	TEST	CLO 2	20.00	5.00
10.	FINAL EXAMINATION	FINAL EXAMINATION	CLO 1	40.00	20.00
11.	FINAL EXAMINATION	FINAL EXAMINATION	CLO 2	60.00	30.00
					100.00

Please ensure all the CLOs as stated in the syllabus are assessed !

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Another example of : **Assessment Management**

A course with only 3 CLOs

COURSE MONITORING MODULE

Input
 Fakulti: FAKULTI KEJURUTERAAN ELEKTRIK DAN ELEKTRONIK
 Sesi / Sem: 20152016 / 2 Program: Degree

Senarai Kursus
 Carian: BEL30403

Bil	Session	Sem	Course Code	Course Name	Submitted / No. Of Section	No. Of Student	Course Coordinator	Total (%) Assessment
1.	20152016	2	BEL30403	REKABENTUK DAN ANALISIS LITAR ELEKTRONIK / ELECTRONICS CIRCUIT ANALYSIS AND DESIGN	0 / 1	31	K001586 - DR. JAIS BIN LIAS	100

Assessment List

NO.	ASSESSMENT NAME	METHOD	CLO	FULL MARK	PERCENTAGE
1.	QUIZ	QUIZ	CLO 1	5.00	5.00
2.	ASSIGNMENT - AFFECTIVE	ASSIGNMENT	CLO 3	5.00	5.00
3.	ASSIGNMENT - COGNITIVE	ASSIGNMENT	CLO 1	5.00	5.00
4.	ASSIGNMENT - PSYCOMOTOR	ASSIGNMENT	CLO 2	5.00	5.00
5.	TEST 1	TEST	CLO 1	40.00	15.00
6.	TEST 2	TEST	CLO 1	30.00	15.00
7.	FINAL EXAM	FINAL EXAMINATION	CLO 1	100.00	50.00
					100.00

All 3 CLOs are assessed

Please ensure all the CLOs as stated in the syllabus are assessed !

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Overall CLOs Achievement Report

Course Code : BEL30403 REKABENTUK DAN ANALISIS LITAR ELEKTRONIK / ELECTRONICS CIRCUIT ANALYSIS AND DESIGN Session / Sem : 20152016 / 1
 Section : SEMUA
 Coordinator : 01586 - DR JAIS BIN LIAS

Overall CLOs ACHIEVEMENT REPORT (Previous) Overall CLOs ACHIEVEMENT REPORT (Current)

OBE Option	CLOs	PLOs	KPI		Achieved KPI		Remarks
			% Students	% Marks	No. Students	% Students	
2	CLO 1	PLO 11 (PA)	50	55	55 / 297	18.52	Not Achieved
2	CLO 2	PLO 10 (DS)	50	55	292 / 297	98.32	Achieved
2	CLO 3	PLO 06 (LLL)	50	55	269 / 297	90.57	Achieved

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Overall CLOs Achievement Report

Course Code : Section : SEMUA Coordinator :
 OBE Matrix Assessment Management Assessment Report By Section List Of Lecturers COI Report Documents (In Progress)

Overall CLOs ACHIEVEMENT REF

Session 20142015 / 1
 Course Code

OBE Option	CLOs	PLOs	KPI	Marks		Students	Students	Students	Remarks
				Students	Marks				
2	CLO 1	PLO 01 (K)	50	55	7 / 38	18.42			Not Achieved
2	CLO 2	PLO 02 (P)	50	55					

HASIL PEMBELAJARAN (LEARNING OUTCOMES):

At the end of this course the student will be able to:

- analyse [] engineering problem using [] theory, techniques and concepts; (PLO1-K-C4)
- display an ability to use appropriate techniques, hardware and software to solve a [] system problem; and (PLO2-PS-P4)
- demonstrate the importance of classical and modern [] system in current technology for sustainable development. (PLO12-EVTS-A3)

The course has 3 CLOs as stated in the syllabus →